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PATENT NUMBER and  
ISSUE DATE

## U.S. UTILITY Patent Application

APPL NUM 10058771	FILING DATE 01/30/2002	CLASS 711	SUBCLASS 118	GAU 218	EXAMINER <i>Verbrugge</i>
<b>**APPLICANTS:</b> Saito Yasuhiko; Miyamoto Kazunori;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-022210 01/30/2001 <i>KV</i>					
PG-PUB <input type="checkbox"/>		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <i>KV</i>				ATTORNEY DOCKET NO 8013-1004	
Verified and Acknowledged Examiners's initials <i>KV</i>					
<b>TITLE :</b> Control circuit for cache system and method of controlling cache system <small>U.S. DEPT. OF COMM./PAT. &amp; TM.-PTO-436L (Rev. 12-94)</small>					

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Assistant Examiner	
<b>ISSUE FEE</b>		Total Claims	
Amount Due	Date Paid	Print Claim for O.G.	
		<b>DRAWING</b>	
		Sheets Drwg.	Figs. Drwg.
		Print Fig.	
		Application Examiner	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	
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